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In Runtime Verification (pp. 485-489). Springer Berlin Heidelberg.

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Web services property sequence chart monitor: a tool chain for monitoring BPEL-based web service composition with scenario-based specifications

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Abstract: Web service composition is a new paradigm to develop distributed and reactive software-intensive systems. Owing to the autonomous nature of basic services, the validation of composite service must be extended from design-time to run-time. Here, the authors describe a novel tool chain called web services property sequence chart monitor to monitor temporal, timing and probabilistic properties in composite service based on scenario-based property specifications called property sequence chart, timed property sequence chart and probabilistic timed property sequence chart, respectively. The tool chain provides a completely graphical front-end that eliminates the need to deal with any particular textual and logical formalism. Furthermore, the framework and implementation detail of the tool chain are also presented. Finally, the feasibility and usability of the tool have been validated by the case studies and performance measurement.

1 Introduction

In recent years, the idea of software as a service has added a new paradigm to the service-oriented architecture (SOA). In SOA, basic services are seen as autonomous agents acting according to certain contracts. For example, through workflow languages such as BPEL [1], service requestors may compose existing basic services to provide more powerful composite services. For such systems, verification is particularly challenging as the overall behaviour depends heavily on the participating agents, which renders the analysis of such systems prior to execution almost impossible [2]. Consequently, run-time analysis techniques, such as run-time monitoring that detects the behaviours of system against the desired properties [3], are being pursued as a lightweight verification technique complementing traditional verification techniques at design-time, such as model checking and testing.

The specifications for monitored properties focus mostly on logic-based or scenario-based specification formalisms [4]. Logic-based formalisms are often more expressive than scenario-based formalisms. However, the logic-based formalisms have the following limitations:

- **Lack of intuitiveness:** Properties that are simply captured and described in intuitive way by the natural languages are not easily specified in logic-based specifications.
- **State-based:** The atomic propositions of a logic-based specification can only be state propositions, where messages (events) cannot be easily expressed. Consequently, it is not straightforward to use these logic-based specifications to specify the properties of event-based web service compositions.
- **Trace error interpretation:** In conventional logic-based run-time monitoring, even if an error trace is detected, it is difficult for the developers to debug the trace and pinpoint the problem due to the internal complexity of logic-based specifications.

The first and third limitations have also been emphasised by Holzmann [5], who recognised that the most underestimated problem in applications of automated tools to software verification is the problem of accurately capturing the correctness requirements that have to be verified; and Dwyer et al. [6] who recognised the difficulty in writing properties correctly.

The second limitation implies great challenge in the characterisation of event synchronisation or causal relation. For example, an informal requirement can be described as: ‘if service A sends message m₁ to service B, and service B sends m₂ to service C (in any order), then C must send m₃ to service A within three time units’. Using traditional logic-based specification to specify this event-based interactions is very hard.
Scenario-based formalisms, such as message sequence chart (MSC) [7] and UML 2.0 sequence diagram (UML 2.0 SD) [8], provide a graphical modelling formalism that is widely accepted in industrial practice. Furthermore, all these formalisms provide event-based notations and, consequently, are suitable to specify event-based web service composition properties easily, intuitively and simply. However, the expressiveness of such kinds of graphical specifications is rather weak due to their ambiguous semantics.

To deal with these problems, Damm and Harel [9] propose live sequence charts (LSC) as a visual formalism for scenario-based requirement specification. LSC makes essential extensions to MSC by adding modalities. The existential and universal modalities represent the provision and mandatory requirements, respectively. The power of LSC lies in that a universal LSC chart can optionally contain a pre-chart, which specifies the scenario which, if successfully executed (or matched), forces the system to satisfy the scenario given in the actual chart body (i.e. the main chart). Furthermore, the LSC language is unambiguous because its semantics is strictly defined [9]. However, the LSC formalism still has the following limitations:

- **Complexity:** Compared with traditional MSC and UML 2.0 SD, LSC is still more difficult to use due to complex concepts such as 'cut' and 'location'. The problem is that we need to devise a new property specification formalism that can overcome the limitations of MSC and SD, and is as close to them as possible.
- **Lack of ability to specify chain properties:** Another main disadvantage of LSC is that it cannot clearly support specifying chain constraints. In fact a chain constraint allows the specification of what can be performed before and after exchanging a message. Chains are very important to specify causes, effects, precedence and response relations.
- **Lack of timed and probabilistic extensions:** Currently, there is only one time version of LSC, called time-enriched LSC. However, the semantics of time-enriched LSC is not clearly defined. Furthermore, to the best of our knowledge, there is no probabilistic version of LSC to specify probabilistic properties, which are also very important for certain software systems.

Consequently, to deal with these limitations, this paper focuses on monitoring BPEL-based web service composition based on newly proposed scenario-based specification formalisms called property sequence chart (PSC) [10], timed property sequence chart (TPSC) [11] and probabilistic time property sequence chart (PTPSC) [12]. We envisage PSC/TPSC/PTPSC as a nice complement to the existing property specification languages for monitoring web service composition, with the following desirable features:

- **Intuitiveness:** PSC/TPSC/PTPSC specifications have the necessary language constructs to describe a variety of causality and chain properties. Furthermore, as extensions of a subset of UML 2.0 SD, they are as close to UML 2.0 SDs as possible and are more intuitive in capturing scenario-based user requirements than the logic-based specifications. Finally, PSC/TPSC/PTPSC can provide a completely graphical front-end that eliminates the need to deal with any particular textual formalism.

- **Expressiveness:** PSC/TPSC/PTPSC can be used to specify temporal, timing and probabilistic properties of BPEL-based web service composition, respectively. Furthermore, the expressiveness of PSC/TPSC/PTPSC has already been validated by property specification patterns (PSPs) [6], real-time specification patterns [13] and probabilistic specification patterns [14].
- **Trace error display:** PSC/TPSC/PTPSC can overcome the trace error interpretation problems by providing the possibility of tracing the error back to the scenario-based requirement specifications. Thus, it is easy for the developers to debug and find the trace errors related to both the system models and the requirements.

In summary, this paper makes the following major contributions: We capture a web service composition scenario to be monitored using PSC/TPSC/PTPSC specifications. For PSC specifications, we first obtain a behaviour-equivalent finite automaton (FA) from this chart according to the formal semantics, then we check whether the run-time traces are the accepting language of the generated FA. For TPSC specifications, we first obtain a behaviour-equivalent timed automaton (TA) from this chart according to the formal semantics, then we check whether the run-time traces are the accepting language of the generated TA. For PTPSC specifications, a probabilistic monitor combining TA and a sequential probability ratio test (SPRT) procedure is automatically generated to check whether the run-time traces satisfy the probabilistic properties. A corresponding tool chain called WS-PSC monitor is developed. To demonstrate the generality of our proposed approach and measure its performance, some evaluations of WS-PSC monitor are performed.

The rest of the paper is organised as follows: Section 2 presents a running example to be used in the paper. This example illustrates temporal, timing and probabilistic properties in web service composition. Section 3 introduces the basic concepts of PSC, TPSC and PTPSC specifications. Section 4 details the framework and implementation of WS-PSC tool. The case studies and performance evaluations of the tool are presented in Section 5. Section 6 compares our approach to related work. Section 7 concludes the paper and gives a list of future work.

## 2 Running example

We use an online medical assistant (OMA) composite service as a running example in this paper. OMA is a system proposed by IBM China [http://www-31.ibm.com/smb/cn/industries/healthcare/] and is becoming popular in China. It provides patients, doctors and managers in medical institutes with useful medical services. OMA includes seven services:

- Medical service agent (MSA) is a medium between patients and medical institutes, which makes an appointment and referral to specialty hospitals.
- Primary care hospital (PCH) is a hospital that offers primary care services to patients, including routine vaccinations and basic consultation.
- Medical insurance centre (MIC) can verify the patient’s medicare that provides patient financing for medical treatments.
- High health sector (HHS) can provide advanced care to patients who cannot be treated in a primary care facility alone.
Medical management institute (MMI) can store simple services. Thus, some timing properties need to be transmitted to MIC for promoting reimbursement process. Furthermore, this information is also recorded in the EMR and transmitted to the essential DLC, MIC and the PCH.

Prop4: After the patient submits a request to the MSA, within 30 s, MSA will send the hospital list returned by MMI to the patient.

Prop5: After completing the diagnosis, within 30 min, PCH will ask MSA to record the diagnostic report into the EMR and transmit the result to the essential DLC, MIC and the PCH.

Sometimes, it is very difficult to assure the strict correctness of timing properties. Recently, there is an increasing research on monitoring probabilistic properties in composite service, since they can be used to formulate reliability, availability, safety and performance requirements. Consequently, two probabilistic properties of the process can be specified as follows:

Prop6: After the patient submits a request to the MSA, within 30 s and with 90% probability, MSA will send the hospital list returned by MMI to the patient.

Prop7: After completing the diagnosis, there is 95% probability that within 30 min PCH will ask MSA to record the diagnostic report into the EMR, transmit the result to the essential DLC, MIC and the PCH.

3 Formal operational semantics of PSC/TPSC/PTPSC

This section will give formal operational semantics of PSC, TPSC and PTPSC for run-time monitoring purpose according to [10-12].

3.1 Property sequence chart

3.1.1 Introduction: PSC is an extended graphical notation of a subset of UML 2.0 SD proposed in [10]. Its design rationale is balancing 'expressive power' and 'simplicity of use'. Fig. 2 shows the PSC graphical elements. A PSC specification has sets of component instances, messages, constraints and operators. Two basic message types are available: arrowMSGs and intraMSGs. The arrowMSGs have three subtypes: Regular, Required and Fail. Regular messages (labelled with $e_{msg}$) are used to define the precondition for a desired (or an undesired) interaction. Required messages (labelled with $f_{msg}$) should never be exchanged and are used to express undesired interactions. IntraMSGs are used to describe constraints that restrict the exchange of messages ($arrowMSGs$). Constraints are classified into two categories: unwanted message constraints and chain constraints. An unwanted message constraint is specified for a set of $intraMSGs$ that the system must not exchange. In other words, an unwanted message constraint describes the event(s) or interactions that are disallowed between two component instances. Chain constraints are defined as a sequence of dependent $intraMSGs$, and are further classified as wanted and unwanted. Wanted chain constraints are satisfied if the
Fig. 1 Main BPEL structure for OMA example

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messages are exchanged following the sequence imposed by the chain specifications. Unwanted chain constraints require that the messages do not occur in the sequence specified in the chain specification.

Constraints are also classified into past constraints and future constraints. Past constraints specify message exchanges, wanted or unwanted, before a specific message exchange event takes place, and future constraints specify the constraints afterwards. Graphically, past constraints are located near the arrow source and future constraints are located near the arrow target of an arrowMSG.

PSC has five operators: loose, strict, parallel, loop and alt, which define how arrowMSGs can be composed. The loose operator is the default operator that defines the order of messages. However, any other messages can occur between these messages. The strict operator explicitly specifies a sequential ordering between a pair of messages, and no other message is allowed between them. The parallel, loop, and alt operators specify parallel merging (i.e. interleaving), iteration and alternative behaviour, respectively.

3.1.2 Semantics: The original operational semantics of PSC is given by Büchi automata [10]. The algorithm Psc2Ba defined in [10] translates PSCs into corresponding Büchi automata. However, Büchi automata can only accept infinite traces and is not suitable for monitoring purpose since run-time traces are finite. Consequently, this paper will use another semantics domain called finite automata (FA) which can accept finite traces. We make use of basic translation rules and compositional rules to translate PSCs into FAs. The basic translation rules are used for directly deriving a FA corresponding to a single arrowMSG (Regular, Required or Fail messages). Then, compositional rules are applied to PSC operators: Par, Loop and Alt. Furthermore, we give a trace-based denotational semantics of PSC in Section 8.1. The correctness of operational semantics can be proved by demonstrating the consistency between the two semantics.

The formal definition of FA for basic rules is defined first.

Definition 1 (FA for basic rules): A FA generated from a basic rule is a six-tuple \( FA_b = (S, \Sigma, s_0, T, G, A) \), where

- \( S \) is the finite set of states;
- \( \Sigma \) is the message alphabet exchanged among different services;
- \( s_0 \in S \) is the initial state;
- \( T \subseteq S \times \Sigma \times S \) is the finite transition set. \( t = (s, \sigma, s') \in T \) is a transition, where \( \sigma \in \Sigma \), \( s, s' \in S \);\n- \( G \subseteq S \) is the set of glue states. Glue state means the current trace satisfies current PSC message and the system has a valid continuation. Glue state is used to merge the initial state of the next \( FA_b \) to form a more complex \( FA_b \).
  - \( A \subseteq S \) is the set of accepting states. The \( FA_b \) describes the complementary behaviours of each basic message. Consequently, accepting state means the current finite trace violates the PSC specification.

In the generated \( FA_b \), other states are called intermediate state set. Intermediate states mean undecided result and need more traces to draw a conclusion.

Basic semantics for PSC: The Regular messages represent the construction of a pre-condition. If a Regular message does not happen, then the monitor does not detect failures (i.e. the property is still undecided or valid). But if a Regular message (or a set of messages) happens then a pre-condition has been satisfied and the continuation of the PSC must be explored. Therefore the generated \( FA_b \)s do not contain any accepting states but they contain glue states that are reached when the Regular messages are exchanged. Fig. 3 shows the basic rules for Regular messages. The \( e \) loose rule (ER1_PSC) represents the rule for Regular message in which if \( a \) happens then it causes a transition to a glue state. The set transition labelled \( la \) (\( la \) is an abbreviation of message label \( C_1, la, C_2 \). we will use this kind of abbreviation in the rest of the rule definition) in the state \( s_0 \) means that other messages can be exchanged before \( a \). The rule \( e \) strict (ER2_PSC) is for the strict operator. Different from the \( e \) loose rule, after having reached the state \( s_0 \), we have a valid continuation to be explored if and only if the next exchanged message is exactly \( a \). ER3_PSC (\( e \) past unwanted message) is the combination of the message \( a \) with the unwanted messages constraint \( b \), where \( b = \{ m_1, m_2, \ldots, m_n \} \) is the unwanted messages set. The idea is that we have a valid continuation if \( a \) happens and during its past no message \( m \in b \) has been exchanged. This is obtained by means of the self-loop labelled \( b \) & \( la \) at state \( s_0 \). ER4_PSC (\( e \) future unwanted message) is the case for the future constraint. In this case, we want to have one message \( m \in b \) after having reached the valid continuation on \( s_1 \). The valid continuation is no longer valid if one message \( m \in b \) is exchanged. ER5_PSC (\( e \) strict future unwanted message) is exactly the intuitive combination of \( e \) strict with \( e \) future. ER6_PSC (\( e \) past unwanted chain) is a combination of unwanted chain \( g \) and message \( a \), where \( g = \{ m_1, m_2, \ldots, m_n \} \) is the message chain. If \( g \) does not happen completely and \( a \) is exchanged, the monitor will reach a valid continuation. ER7_PSC (\( e \) past wanted chain) is the wanted case. Consequently, if \( g \) happens completely and \( a \) is
exchanged, the monitor will reach a valid continuation. ER8_PSC and ER9_PSC are the future cases of unwanted and wanted chains.

The semantics rules for Required message are shown in Fig. 4. A Required message is a message that must be exchanged within finite traces (in practice, we can judge whether the number of traces reaches a particular large number). In the case of RR1_PSC (r loose), if the message does not happen within finite traces, then the monitor will go to the accepting state and the property violation is detected. A valid continuation can be reached if a happens within finite traces. RR2_PSC (r strict) shows that if any other message but a (i.e. !a in the figure) happens while in state s0, then the monitor goes to the accepting state immediately and there is no chance for satisfying the property. RR3_PSC (r past unwanted message) raises an
Fig. 5 Operational semantics of Fail messages of PSC

operators can be defined based on basic operators Merge and Alternative.

Definition 2 (Merge for two FA bs): Given two FA bs generated by basic rules, \( FA = (S, \Sigma, s_0, T, G, A) \) is the merge composition of \( FA_1 \) and \( FA_2 \), denoted as \( FA = FA_1 FA_2 \), where

- \( FA\cdot S = FA_1\cdot S \cup FA_2\cdot S \)
- \( FA\cdot \Sigma = FA_1\cdot \Sigma \cup FA_2\cdot \Sigma \)
- \( FA\cdot s_0 = FA_1\cdot s_0 \)
- \( FA\cdot T = FA_1\cdot T \cup FA_2\cdot T \cup T_{glue} \), where \( T_{glue} = \{\forall s, s\}' \)
- \( FA\cdot G = FA_1\cdot G \cup FA_2\cdot G \)
- \( FA\cdot A = FA_1\cdot A \cup FA_2\cdot A \)

The Merge and Alternative operators will generate new FAs with \( \varepsilon \) transitions. However, an equivalent FA without \( \varepsilon \) transitions can be obtained through standard automata operations [15]. Furthermore, they can be extended to a finite number of FA bs.

Definition 4 (Merge for n FA bs): Given n FA bs generated by basic rules, \( FA = (S, \Sigma, s_0, T, G, A) \) is the merge composition of n basic FA bs, where

- \( FA\cdot S = FA_1\cdot S \cup \ldots \cup FA_n\cdot S \)
- \( FA\cdot \Sigma = FA_1\cdot \Sigma \cup \ldots \cup FA_n\cdot \Sigma \)
- \( FA\cdot s_0 = s_0 \)
- \( FA\cdot T = FA_1\cdot T \cup \ldots \cup FA_n\cdot T \cup \{s_0, \varepsilon, FA_1\cdot s_0\} \cup \{s_i, \varepsilon, FA_i\cdot s_0\} \)
- \( FA\cdot G = FA_1\cdot G \cup \ldots \cup FA_n\cdot G \)
- \( FA\cdot A = FA_1\cdot A \cup \ldots \cup FA_n\cdot A \)

Definition 5 (Alternative for n FA bs): Given n FA bs generated by basic rules, \( FA = (S, \Sigma, s_0, T, G, A) \) is the alternative composition of n basic FA bs, where

- \( FA_1 FA_2 FA_3 \ldots FA_n = ((FA_1 FA_2) FA_3) \ldots FA_n \)
Based on operators **Merge** and **Alternative**, the operators of **Alt**, **Par** and **Loop** can be defined as follows:

**Definition 6 (Alt operator of PSC):** \( \text{Alt}(\omega_{1:j1}, \omega_{2:j2}, \ldots, \omega_{r:jr}, r) \) has \( r \) parts of selections.

**Definition 7 (Par operator of PSC):** \( \text{Par}(\omega_{1:j1}, \omega_{2:j2}, \ldots, \omega_{r:jr}, r) \) has \( r \) parts of selections. Owing to combinatorics, the number of message sequences generated by parallel composition is
\[
\text{num(Par)} = (n!/(k_1!k_2! \cdots k_r!)) \text{, where } k_1, k_2, \ldots, k_r \text{ are the numbers of these } r \text{ parts of messages.}
\]

**Definition 8 (Loop operator of PSC):** \( \text{Loop}(\omega^{i:j}, m, n) \), the upper bound of loop is \( n \) and lower bound of loop is \( m \).

**Definition 9 (FA for whole PSC):** A FA generated from the whole PSC is a six-tuple \( F_A = (S, \Sigma, s_0, I, F, \delta) \) with the different from \( F_A.b \) being that the glue state set \( G \) is replaced by the final state set \( F \).

3.2 Timed property sequence chart

3.2.1 Introduction:” In UML SD, time stamps can be attached to messages. Thus, constraints on a lower and an upper time bound between two continuous messages can be formulated. Similar to the messages in UML SD, each message in PSC can also be annotated with time constraints, or time constructs for short.

As shown in Fig. 6a, a regular message \( e:a \) is extended as \( e:a; x < t \), and then a clock \( v \) is reset. Since it is a regular message, the system will not raise an error if \( e:a \) does not happen within the desired time constraint. However, when \( e:a \) is replaced by \( f:a \), it means a required message \( r:a \) must be exchanged before time \( t \). If it does not happen, the system will raise an error. When \( e:a \) is replaced by \( f:a \), it means that the system will go to an error state when \( f:a \) is exchanged before \( t \).

3.2.2 Semantics:” In [11], we have defined the formal translational semantics that maps a TPSC specification into a corresponding timed Büchi automaton (TBA). TBA can only accept infinite traces. Similar to PSC, we also use another formalism called TA as the corresponding semantics domain. We can also define the time trace-based denotational semantics of TPSC. The correctness of the translating rules can be proved by ensuring the consistency between the two semantics.

**Definition 10 (Clock constraints):** For a set of clocks \( X \), a clock constraint \( \delta \) from the set of clock constraints \( \Phi(X) \) can be defined as follows [16, 17]
\[
\delta = \pre{\delta} - x < c | x \geq c | x < c | ¬\delta_1 \land \delta_2
\]
where \( x \in X \) is a clock variable, and \( c \in \mathbb{N} \) is a constant.

**Definition 11 (Evaluation functions):** Several evaluation functions have been defined. The function \( v(x) \) of a clock \( x \) and checks if the value fulfills the constraint \( \delta \). The function \([\delta] = \{ v(x) | v(x) < \min(\delta(x)) \} \) and \([\delta] = \{ v(x) | v(x) > \max(\delta(x)) \} \), which respectively describe a clock values

Fig. 6 Extending PSC with time constructs
that do not fulfil the clock constraint and happen before and after the clock constraint.

For example, if \( \delta(x) = x \geq 2 \land x \leq 4 \), \( [\text{pre} (\delta)] = \{ v(x) | v(x) < 2 \} \), and \( [\text{succ} (\delta)] = \{ v(x) | v(x) > 4 \} \).

Definition 12 (Clock reset): A clock reset for a clock \( x \) is defined as \( v(x) = 0 \). Normally, we denote \( \psi \) as a set of clock reset.

The formal definition of TA is defined first.

Definition 13 (TA for basic rules): A TA generated from a basic rule of TPSC is a seven-tuple \( TA = (\Sigma, S, s_0, F, G, \Phi, \delta) \), where:

- \( \Sigma \cup 1 \) is a finite set of simple message labels exchanged in the system, where \( '1' \) means that any messages can happen.
- \( S \) is a finite set of states.
- \( s_0 \in S \) is the initial state.
- \( F \subseteq S \) is the finite set of accepting states. Note that TA also represents the complementary behaviours of TPSC, so the accepting state represents a failure of the system.
- \( G \subseteq S \) is the set of glue states. They connect the initial state of another TA to form a new TA, \( TA' \).
- Clock is a finite set of clocks, as defined in Definition 10.
- \( T \times S \times \Sigma \times \Phi (\text{Clock}) \times 2^{\text{Clock}} \) is a set of transitions.

In the following, we will define the formal translational semantics that maps a TPSC specification into a corresponding TA. The rules are categorised into basic and compositional rules. Basic rules identify how to translate a single message in a TPSC specification into a TA, whereas compositional rules show how to compose these basic automata according to structured operators, such as \( \text{par} \), loop and \( \text{alt} \).

Basic rules for TPSC. Fig. 7 shows the corresponding rules for generating basic TAs for Regular messages. The translation rules are similar to the rules of PSC, with each translation denoted by clock constraint or clock reset. The symbols \( \delta, \delta' \) and \( \delta'' \) are used to represent clock constraints for messages, past constraints and future constraints, respectively, as defined in Definition 10. \( \psi \) and \( \psi' \) represent the sets of reset clocks for messages, past constraints and future constraints, respectively, as defined in Definition 12. ER1_TPSC(e loose) rule shows if \( a \) is not exchanged and other messages happen, the TA stays at \( S_0 \). When \( a \) happens within \( \delta \), the TA stays at \( S_0 \). Otherwise, the TA will go to the glue state if \( a \) happens in line with \( \delta \). ER4_TPSC is the strict case, if \( a \) happens under \( \delta \) the TA will go to the accept state that requires the messages in \( b \) do not happen under \( \delta' \). ER5_TPSC is the strict case of future unwanted message. In ER6_TPSC, when the chain \( g \) does not happen completely under \( \delta' \) and \( a \) happens under \( \delta \), the TA will go to the accept state. In ER7_TPSC, if \( g \) happens completely satisfying \( \delta' \) and \( a \) happens satisfying \( \delta \), the TA will go to the accept state. In ER8_TPSC, when some messages in \( g \) happens under \( \delta' \) or \( g \) has not happened completely until \( \text{succ} (\delta') \), the TA will go to the accept state.

In the following, we will define the formal translational semantics that maps a TPSC specification into a corresponding TA. The rules are categorised into basic and compositional rules. Basic rules identify how to translate a single message in a TPSC specification into a TA, whereas compositional rules show how to compose these basic automata according to structured operators, such as \( \text{par} \), loop and \( \text{alt} \).

![Fig. 7 TAs for Regular messages of TPSC](image-url)
continuation. In RR4_TPSC, when \( a \) happens within \( \delta \), the TA will go to the glue state. Then the TA also requires that the messages in \( b \) have not happened under \( \delta'' \) at the glue state. RR5_TPSC is the strict case, with the difference from RR4_TPSC being that no other messages are allowed before \( a \) within \( \delta \). RR6_TPSC is the case with past unwanted chain. If \( g \) does not happen completely under \( \delta' \), and \( a \) is exchanged under \( \delta \), TA will go to the glue state. Otherwise when \( g \) happens completely under \( \delta' \), or \( g \) does not happen completely but \( a \) does not happen under \( \delta \), the TA will go to the accepting state and an error is raised. In RR7_TPSC, if \( g \) happens completely and \( a \) happens under \( \delta \), the TA will go to the accepting state. Otherwise TA will go to the glue state. The only difference between Rule RR9_TPSC and RR8_TPSC is that \( g \) is expected to happen completely under \( \delta'' \).

The rules for fail messages of TPSC can be defined in a similar way. As there are only two differences for rules of Fail message with respect to the Required rules, we skip the detailed presentation of these rules. First, the semantics of a Fail message is inverse to the semantics of a Required messages, in that glue states and accepting states are interchanged. Secondly, since other messages cannot follow a fail message, there is no clock reset in final transitions of the generated TAs.

**Compositional rules for TPSC:** Using these basic rules, basic messages in a TPSC specification can be translated into the corresponding simple TAs. However, the real-time property represented by a TPSC specification is usually composed of many basic messages with structured operators, such as \( \text{alt} \), \( \text{par} \) and \( \text{loop} \). Therefore, compositional rules are also need to be defined to compose simple TAs according to the structure of complex TPSCs. There are two kinds of compositional rules. One is to merge TAs in a sequential and alternative way, that is, Merge and Alternative. The other includes \( \text{alt} \), \( \text{par} \) and \( \text{loop} \) structured operators. These operators can be defined in a similar way as PSC.

### 3.2.3 Expressiveness

The expressiveness of the TPSC specification language has been tested with the real-time specification patterns proposed by Konrad and Cheng [13]. These real-time specification patterns, which are a timed extension of the un-timed specification patterns [6], are proposed to support quantitative reasoning about time. The patterns are classified into duration (minimum duration, maximum duration), periodic (bounded recurrence) and real-time order (bounded response, bounded invariance). Each real-time specification pattern can be further associated with five scopes (globally, before, after, between and, and after until) as defined in [6]. In our previous work, we have already used TPSC to represent all these patterns [11].

### 3.3 Probabilistic timed property sequence chart (PTPSC)

#### 3.3.1 Introduction

According to the same design rationale of PSC and TPSC, we later designed a new probabilistic scenario-based specification called PTPSC [12]. Each message or operator can be annotated with a probability. Furthermore, according to the idea of LSCs [9], we add the pre-chart to PTPSC to describe a trigger condition for starting a monitoring run. The messages in the pre-chart are restricted to the regular type. Following the
pre-chart in PTPSC, a main chart is enriched with a probability operator. PTPSC only supports one probability construct. However, if a probabilistic property has more than one probability, it can be modelled by a joint probability of multiple messages.

3.3.2 Semantics: One challenge of defining the formal semantics of PTPSC specifications is that there is no clear accepting or rejecting condition like that of traditional temporal or timing specifications. To overcome this problem, we propose to use sequential statistical hypothesis testing based on a null hypothesis ($H_0$ the system fulfills the probabilistic property) and an alternative hypothesis ($H_1$ the system does not fulfill the probabilistic property). The specific statistical hypothesis test we use is the SPRT [18] that bounds the probabilities of false accepting or rejecting the null hypothesis to two test parameters $\alpha$ and $\beta$, respectively. Consequently, the operational semantics of PTPSC is defined by two parts:

- Based on the defined syntax, we can translate a PTPSC property into a TA using a syntax directed translator (SDT) [19]. The translation rules of SDT are also categorised into basic and compositional rules [11].
- We design an algorithm to do sequential hypothesis testing. The inputs of this algorithm are the timed automata (TA) for PTPSC, two hypothesis $H_0$ and $H_1$ that are defined based on the probability $p$ specified in the PTPSC and other hypothesis parameters such as $\alpha$ and $\beta$. The outcome of the hypothesis test is that $H_0$ is accepted, or $H_1$ is accepted, or undecided, implying more samples are needed. In practise, as shown in Fig. 9 the test procedure compares the number of correct samples $d_m$ of $m$ experiments with two functions $c_0(m)$ and $c_1(m)$. If $d_m > c_0(m)$, then $H_0$ is accepted. If $d_m < c_1(m)$, then $H_1$ is accepted. Otherwise, when $c_0(m) > d_m > c_1(m)$, it cannot be decided and more samples are needed.

3.3.3 Expressiveness: PTPSC is designed to represent probabilistic properties. In order to measure the expressiveness of the PTPSC specification language, we test it with respect to the PSP system ProProST (Probabilistic Property Specification Templates) of common probabilistic properties proposed by Grunske [14]. The specification pattern system contains eight generic patterns. PTPSC can represent seven patterns including the Transient State Probability pattern, the Probabilistic Invariance pattern, the Probabilistic Existence pattern, the Probabilistic Precedence pattern, the Probabilistic Response pattern, the Probabilistic Constrained Response pattern, and Steady State Probability [4]. Note that the PSP Steady State Probability requires long running behavioural analysis of the system and cannot be used for monitoring. Consequently, this pattern cannot be expressed with PTPSC.

3.4 Example

The informal requirements of the running example can be represented by PSC, TPSC and PTPSC specifications, corresponding to temporal, timing and probabilistic properties, respectively.

For Prop1 to Prop3, PSC specifications can be used to represent them. As an example, Fig. 10 shows the PSC specification of Prop1. It is composed of four messages: a Regular message, a Required message with future unwanted chain constraint and two Required messages. Using the translating rules in basic rules (ER1_PSC, RR4_PSC, RR1_PSC and RR1_PSC) and compositional rules (Merge), the generated FA for Prop1 is shown in Fig. 11. Within finite traces, the monitor can stay on $S_0$ to $S_4$ and

Fig. 9 Graphical interpretation of the SPRT

Fig. 10 PSC specification for Prop1

Fig. 11 Generated FA for Prop1
eventually goes to final state $S_5$ when the desired messages happen. However, if $a_3$, $a_5$, $a_7$ and $a_9$ do not happen within finite traces, the system will raise failures.

For Prop4 and Prop5, TPSC specifications are used to represent them. Fig. 12 shows the TPSC specifications of Prop4 as an example. The structure of this property is similar to Prop1 except adding some timing information.

Fig. 13 shows the PTPSC specifications of Prop7 as an example. It is composed of a total of six messages: first, a Regular message and a Required message with past chain constraint. Then there are two parallel parts, each with a Regular message and a Required message. After adding the time reset clock and time constraints to each message, the first message is a trigger message, and all the messages in the main chart may happen with a probability of 95%.

### 4 WS-PSC tool

This section first shows the framework of WS-PSC monitor and then shows how to monitor temporal, timing and probabilistic properties in WS-BPEL process with our running example.

#### 4.1 Framework overview

The framework of the prototype tool WS-PSC monitor is illustrated in Fig. 14, consisting of four main components: Interceptor, Observer, Translator and Analyzer, respectively.

- **Interceptor**: Aspect-oriented programming (AOP)-based approach [20] is used to extend the existing BPEL execution. We consider business processes as core concerns and the monitored messages as cross-cutting concerns. The messages that describe the complex interaction between
several service components are related to these service components in the composite service. The same monitoring codes need to be repeatedly added into BPEL business processes for the corresponding service components. It results in lowering the maintainability and reusability of codes because the monitoring codes intertwined with business processes. Monitoring aspects of a composite service can be implemented with AspectJ, which is a seamless extension of Java language. It has its own constructs, such as aspects, join point, point cut and advice, etc. A joint point refers to a well-defined point during program execution. The set of joint points are called pointcut. Similar to methods, advices attached to pointcut define the extra behaviour of pointcut. Aspects are the implementation of modularity unit of cross-cutting, which include point cut, advice and type declaration. According to the identified joint points, the aspects and goal application are weaving together. The weaving process may be static (at compilation) or dynamic (at run-time). We defined a set of joint points of certain monitoring aspects, namely, the combined points between monitoring aspects and business logic are defined. A pointcut similar to Boolean operator in programming language selects a certain joint point to execute monitoring aspects. The combination of several pointcuts can observe some kinds of different joint points, which supports monitoring several service states when the business processes concurrently execute.

According to user’s requirements, PSC, TPSC and PTPSC specifications are used to represent the monitored properties. Then the monitoring aspects are defined to intercept the interaction messages between basic services such as ‘invoke’, ‘receive’ and ‘reply’. Finally, the defined aspects and original services are weaved together to intercept the run-time messages of composite services and add corresponding time stamps to each message.

- **Observer**: Observer collects the run-time messages intercepted by Interceptor. Usually, we can only observe SOAP messages that are related to activities. Message occurrence means activity occurrence. Global observer or local observer is used to observe message exchange or activity occurrence. Global observer can observe activities and messages at process level, concerning several services. Local observer can only observe activities or messages relative to one individual service. We focus on locally observing messages sent or received by individual service. Two observers are designed to intercept messages in PSC and TPSC, respectively. Observer1 can only collect the messages and record them in order to further check whether these messages satisfy PSC properties. Observer2 can not only collect the messages but also record the corresponding time stamps to further check these messages against TPSC and PTPSC properties.

- **Translator**: Translator is used to translate PSC, TPSC and PTPSC specifications into formats that can be accepted by Analyzer. We designed three translators for these specifications. Translator1 can translate PSC specifications into semantics domain FA. Translator2 can translate TPSC specifications into the semantics domain TA. Translator3 can translate PTPSC specification into semantics domain TA and a SPRT process.

- **Analyzer**: On receiving the messages from the Observer, the Analyzer has the ability to verify at run-time whether a property has been satisfied or not. We designed three different analysers for PSC, TPSC and PTPSC, respectively. The inputs of these algorithms are intercepted messages and the corresponding semantics domain, FA, TA and TA with SPRT process. Similar to the approach in [21, 22], we use the three-valued semantics for the monitoring outputs: ‘true, false or undecided’. Note that in the generated FA or TA, there are three kinds of states: accepting state, final state and internal state. Since FA or TA describes complementary behaviours of PSC or TPSC properties, accepting state means property violation, final state means property satisfied while intermediate state means undecided.

Analyzer1 improves FA-based analysing algorithm proposed in [23]. In [23] we obtain the monitoring result according to a message set. In this paper, we obtain the monitoring result based on each intercepted message since we need to draw conclusion immediately. The main logic of Analyzer1 is shown in Fig. 15. For each intercepted message $m$, Lines 1–2 show that Analyzer1 will go to next state if $m$ matches the corresponding transitions in $FA$.

![Algorithm Analyzer1()](image1)

**Algorithm Analyzer1()**

**Input**: current intercepted message $m$ and a property PSC represented by a Finite Automaton $FA$.

**Output**: True, false or undecided.

1. If $m$ matches transitions in $FA$ from $currentState$ then
2. $FA$ goto $nextState$
3. Switch $nextState$ in $FA$
4. Case $nextState$ in accepting state
5. Return(false with error trace)
6. Case $nextState$ in final state and scope is global
7. goto initial state
8. Case $nextState$ in final state and scope is not global
9. Return(true)
10. Case $nextState$ in internal state
11. Return(undecided)

![Algorithm Analyzer2()](image2)

**Algorithm Analyzer2()**

**Input**: A monitored message set $J$ with timing information and a property $TA$ represented by a Timed Automation.

**Output**: True, false or undecided.

1. If $m$ and time matches transitions in $TA$ from $currentState$ then
2. $TA$ goto $nextState$
3. Switch $nextState$ in $TA$
4. Case $nextState$ in accepting state
5. Return(false with error trace)
6. Case $nextState$ in final state and scope is global
7. goto initial state
8. Case $nextState$ in final state and scope is not global
9. Return(true)
10. Case $nextState$ in internal state
11. Return(undecided)

![Algorithm Analyzer3()](image3)

**Algorithm Analyzer3()**

**Input**: A monitored message set $J$ with timing information and a property $TA$ represented by a Timed Automation with a probability $p$.

**Output**: True, false or undecided.

1. According to the result of Analyzer2
2. If $nextState$ goto accepting state in $TA$ then
3. Record(0 in Result)
4. If $nextState$ goto final state in $TA$ then
5. Record(1 in Result)
6. Use Result as the input for SPRT process
7. Switch $sm$
8. Case $sm$ is greater than $c(m)$
9. Return(true)
10. Case $sm$ is less than $c(m)$
11. Return(true)
12. Case $c(m)$ is greater than $d(m)$
13. Return(undecided)

![Fig. 15](image4) **Three analysing algorithms**
Lines 3–11 shows four cases during continuous monitoring: if \( FA \) goes to an accepting state, an error trace will be detected; if \( FA \) goes to the final state and the property scope is global, it will restart at the initial state again; if \( FA \) goes to the final state and the property scope is not global, it will return true; if \( FA \) goes to an intermediate state, it will return undecided.

Analyzer2 improves TA-based trace analysing algorithm proposed in [24]. The main idea of Analyzer2 is shown in Fig. 15b. The main difference between Analyzer1 and Analyzer2 is that Analyzer2 considers timing information. Consequently, it can not only analyse the message but also check that the timing stamps satisfy the transitions in TA.

Analyzer3 implements a monitor combining TA and SPRT process proposed in [4]. The main logic of Analyzer3 is shown in Fig. 15c. According to the result of Analyzer2, if \( nextState \) goes to an accepting state, a ‘0’ is recorded into \( Result \); if \( nextState \) goes to the final state, a ‘1’ is recorded into \( Result \). Then, \( Result \) is used as input to the SPRT process according to the semantics of PTPSC. Finally, the checking results are returned based on lines 7–13.

The flow of the WS-PSC monitor framework is also shown in Fig. 14 and divided into the following steps:

1. AOP-based approach [20] is used to extend the ActiveBPEL engine with Interceptor. Interceptor that is the core component in our framework allows for defining monitor-related pointcuts and advises in AspectJ. Our approach can define pointcuts for the following events of the engine: Engine starts or stops, BPEL process construction or destruction and key activities of BPEL process such as invoke, receive and reply. For example, in order to monitor the whole activities exchanged by the composite service and the corresponding timing information, we can define the following aspect in AspectJ to record the activities:

   ```java
   1 public aspect InterceptReceive_Log{
   2   pointcut log_Receive():
   3   execution(* receive.* ->receive(.*));
   4   before ():log_Receive()
   5   Date date=new Date();
   6   SimpleDateFormat df=new
   7   SimpleDateFormat("yyyy-MM-dd hh:mm");
   8   Signature setthispoint.getSignature();
   9   MLogFile.println(df.format(date)+
   *[Monitor LOG] Entering:[receive]:="*toToString();
   ```

   In lines 2 and 3 method receive () of an arbitrary class (*) found in the receive package of the ActiveBPEL engine implementation is intercepted. Since the invoke activity only has input parameters, we need to record and add the time stamps for the parameters. Lines 4–9 record the input parameters and add the corresponding timing stamps by the use of ‘before’ method. For activities that just have two parameters such as invoke activity, we have to use before() and after() method to intercept the input and output parameters. Note that if only PSC specifications need to be monitored, we just need to intercept the parameters of the activities without adding timing stamps. Finally, AspectJ class files are generated from monitoring aspects by the compiler. AspectJ class files and original services in ActiveBPEL engine are weaving together. During the execution of business logic, monitoring aspects are dynamically inserted into business process for run-time monitoring.

2. According to property types, the ‘Observer’ can classify the intercepted messages. If the property type is temporal, ...
the Observer can only record all the interaction messages; if the property type is timing or probabilistic, the Observer needs also to record the time stamps for each interaction message. These execution messages are translated into a format that can be accepted by Analyzer. The translated execution message has the following format:

\[
\text{time stamp } [\text{Monitor \ LOG}] \{\text{Entering/Exiting}:[\text{receive}]/[\text{invoke}]/[\text{reply}] \text{messageOperation}(\text{parameter})]
\]

where Entering means input message, whereas Exiting means output message. An execution message sequence of OMA interacting with a patient and the corresponding time stamps are shown as follows.

3. The informal requirements are represented by PSC, TPSC and PTPSC specifications, corresponding to temporal, timing and probabilistic properties, respectively. Example property representations have been shown earlier in Section 3.4.

4. We can use the three translators of the WS-PSC monitor to translate PSC, TPSC and PTPSC into FA and TA, and a combination of TA and SPRT process, respectively. From Prop1 to Prop4, by using translator1, they can be translated into the corresponding FA. For example, the generated automata for Prop1 is shown in Fig. 11. For Prop4 and Prop5, by using translator2, they can be translated into the corresponding TA. For Prop6 and Prop7, the generated TA are the same as those of Prop4 and Prop5. However, we also need to use the SPRT process to calculate the probabilities.

5. Analyzers first receive the intercepted messages and the properties represented by PSC, TPSC and PTPSC, then check whether the run-time information satisfies the desired properties. If the property type is temporal, Analyzer1 is used. If the property type is timing, Analyzer2 is used. If the property type is probabilistic, Analyzer3 is used.

| Table 1 Informal descriptions and the corresponding patterns of the monitored temporal properties |
|---|---|---|---|
| Id | Informal descriptions | Patterns | Results |
| TA (Prop8) | after sending a patient’s medical parameters to the Lab and TA receives a ‘high’ result, TA will immediately notify the assistants nearest to the patient | 2 stimulus-2 response; scope global | yes |
| TA (Prop9) | after sending a patient’s data to the Lab and the TA receives a ‘needDiagnosis’ result, the doctor will diagnose the patient and before that he must receive a notification from TA | precedence; scope after | yes |
| TA (Prop10) | if for a certain patient the number of ‘high’ results is more than three times, the TA also needs to notify the doctors to diagnose the patient | 3 stimulus-2 response; scope global | no |
| TA (Prop11) | if the number of notifications to help a certain patient is less than five times before the end of a process, the TA should also notify doctors to diagnose the patient to see whether the patient is fine | 5 stimulus-2 response; scope global | yes |
| OJA (Prop12) | if the user’s credit account is invalid, he (or she) cannot get a video | response; scope absence | no |
| OJA (Prop13) | if the credit account of the user is detected to be valid, and he cannot get the video, there must be a message showing there is not enough money in the account | precedence; scope after | yes |
| OJA (Prop14) | if there is no satisfied video in KB, VA will send request to other providers, then some providers respond to the request. After VA provides a user with a video list (with respect to textual description of related service information) that is composed of all periodically collected responses, the user reads the video list and selects one of the providers for getting and watching the video | 1 stimulus-2 response and 1 stimulus-2 response; scope global | yes |
| OJA (Prop15) | when getting a video, the user can either watch the video, or she can either watch the make request to choose another video, but for the second case she have to choose the reason (e.g. the poor quality of the current video) and cancel the video | response; global or precedence chain; scope after | yes |
| TM (Prop16) | if the user invokes the travel service (i.e. he/she sends the travel requirements to the TM service), TM service will respond to the user with the result of three service invocationsfindDuration, calculateTime and checkSchedule | existence; scope between and | yes |
| TM (Prop17) | after the user orders the desired travel, the travel schedule will be sent to the user eventually. However, between these two messages, TA must successfully book the Hotel, Traffic and arrange a meeting with Database | existence; scope between and | yes |
Table 2 Informal descriptions and the corresponding patterns of the monitored timing properties

<table>
<thead>
<tr>
<th>Id</th>
<th>Informal descriptions</th>
<th>Patterns</th>
<th>Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>TA</td>
<td>after sending a patient’s medical parameters to the Lab, and the TA receives a ‘high’ result within 1 hour, the TA will immediately notify the assistants nearest to the patient and must receive a response within 1 hour if for a certain patient the number of ‘high’ results is more than three times during a week, the TA also needs to notify the doctors to diagnose the patient within one day if a patient’s medical parameters to the Lab, TA receives a ‘high’ result within 1 hour. TA will immediately notify the assistants nearest to the patient and must receive a response from them within 1 hour.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>(Prop19)</td>
<td></td>
<td>bounded 1 cause 2 response; scope after bounded 3 cause 2 response; scope global bounded 1 cause 2 response; scope after bounded 5 cause 2 response; scope global bounded response; scope global bounded 2 cause 1 response and bounded 1 cause 2 response ; scope global bounded 1 cause 4 response; scope global</td>
<td></td>
</tr>
<tr>
<td>OJA</td>
<td>if there is no satisfied video in KB, VA will send the query request to other providers, then some providers respond to the request within 10 seconds. After VA provides a user with a video list (with respect to textual description of related service information) that is composed of all periodically collected responses, the user will read the video list and selects one of the providers for getting and watching the video within 20 seconds when watching a video, a user does not satisfy with the poor quality of the current video, she cannot watch the video for more than half a minute</td>
<td></td>
<td></td>
</tr>
<tr>
<td>(Prop20)</td>
<td></td>
<td>bounded 2 cause 1 response and bounded 3 cause 2 response; scope global maximum duration; scope global bounded 1 cause 3 response; scope global bounded 1 cause 4 response; scope global</td>
<td></td>
</tr>
<tr>
<td>TM</td>
<td>if the user invokes the travel service (i.e. he/she sends the travel requirements to the TM service), the probability that the TM service responds to the user with the result of three service invocations findDuration, calculateTime and checkSchedule within 3.2 seconds after the user orders the desired travel, the travel schedule will be sent to the user within 10 seconds</td>
<td></td>
<td></td>
</tr>
<tr>
<td>(Prop21)</td>
<td></td>
<td>bounded 1 cause 3 response; scope global</td>
<td></td>
</tr>
<tr>
<td>OJA</td>
<td>if the credit of a user is verified to be valid, the user can eventually obtain a video within 2 minutes</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The analysis results are then shown to designers. They can further analyse and correct the possible errors in the system. In our example Prop1, Prop2 and Prop3 are validated as correct, whereas Prop1 and Prop3 are false. From the error trace of Prop3, we find that the users can obtain patient’s diagnostic records from EMR without patient’s permission. After adding a new activity [invoke]GetPermission to the corresponding patient whose medical data is used in the BPEL process, Prop3 holds. From the error trace of Prop3, we find that it is possible that within 30 seconds, the activity [invoke]DeliveryLogisticsCompanies (detailMedicine) does not happen. Consequently, the TA for Prop3 goes to the accepting state and a violation against Prop3 is observed.

In order to check probabilistic property, we systematically inject the correct implementations with a predefined failure rate\(fr\). To simulate a failure, a correct event such as ‘confirmOK’ is replaced by a ‘confirmFail’ or a ‘confirmTimeout’ message. In each experiment, we set statistical test parameters \(\alpha = 0.1\), \(\beta = 0.1\) and indifferent region as \(H_0 - H_1 = 0.04\). We injected a failure that is 2% below or above the indifferent region. The validated results for properties Prop5 and Prop6 are shown in Fig. 16. For Prop5, we injected 9% (in the indifference region), 7% (2 above \(H_0\) and 15% (2 below \(H_1\)). For Prop6, we injected 7% (in the indifference region), 3% (2 above \(H_0\) and 11% (2 below \(H_1\)). From the curves, we can see that the test correctly accepts or rejects \(H_0\) for these samples. However, the number of samples are different due to the different probabilities. For Prop5, 100 samples are not enough to draw the conclusion. For example, it needs 125 samples to accept \(H_0\). When the probability is set at more than 98%, the tool cannot draw any conclusion within 100 samples. Furthermore, when the probabilities are in the indifferent region, it is very hard to draw any conclusion with our monitor.

5 Experimental validation

In previous sections, we have already shown how to use WS-PSC monitor to monitor temporal timing and probabilistic properties in composite service with PSC/TPSC/PTPSC specifications. To demonstrate the generality of WS-PSC monitor, this section will first apply it to three other case studies and then conduct some experiments to analyse its performance. The tool has been developed on the Eclipse Rich Client Platform (RCP). It has 122 Java classes and about 20000 lines of codes [25].

5.1 Case studies

In this subsection, we will describe three case studies: TA (TelecommunicationAssistant), OJA (OnTheJobAssistant) and TM (TravelManagement). These three case studies come from real industrial requirements and have already been widely used in web service research.

TelecommunicationAssistant [26, 27] composite service (TA) is a software- and telecommunication-based service that is designed to help patients who need daily assistance in remote areas. TA service interacts with four partner
services, including Patient, Medical Laboratory (Lab), Assistant and Doctor. Patient needs basic medical support in order to control their health. Medical Laboratory (Lab) can analyse the medical parameters of patients and replies to TA by sending the results. Assistant may be a patient’s relative who will take over the role of a medical assistant. Doctor is in charge of medical decisions, such as changes of treatments or diagnosis.

OnthelohAssistant (OJA) [28] is a composite service to provide specialists with a variety of video help, consisting of four services: Knowledge Base (KB), Virtual Assistant (VA), Bank and Other Providers. KB can store various video information. VA is a medium between a user and KB, and submits a user’s search request to KB. Bank is responsible for verifying whether the user’s credit is valid. Other Providers: whenever video information required by the user is unavailable in KB, VA submits the user’s search request to other providers.

Travel Management (TM) [29] is a composite service used to help users book a travel package online that suit their requirements. The system interacts with the following participants and services: User, DataBase (DB), Hotel Service (HS) and Traffic Service (TS). The DataBase stores various information about travel. The Hotel Service and Traffic Service provide the corresponding hotel and traffic service.

WS-PSC monitor is used to analyse 10 temporal properties, 10 timing properties and 6 probabilities properties selected from the three case studies.

Table 1 (Prop8 to Prop17) shows the informal descriptions, the corresponding specification patterns and the monitoring results of the temporal properties of the three case studies. To show the usability of our scenario-based specification most properties belong to chain patterns which are not easily represented by traditional temporal logic-based specifications. As a simple example, Prop11 is a 5 Stimulus-2 Response pattern which is very hard to be represented by temporal logic formula.

The results indicate that Prop10 and Prop12 are false. To identify the cause of the violations, we review the interactions between all the BPEL processes. According to the violation trace for Prop10, we find that after the message [invoke]alarm(high) happens more than three times, the message [invoke]changeDiagnosis has not been sent to the doctors. Consequently, the Analyzer will go to the accepting state and a violation against Prop10 is observed.

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results of the timing properties of the three case studies. According to the results, Prop20, Prop22 and Prop25 are false. Prop20 and Prop22 fail for the same reasons as those of Prop10 and Prop12, respectively. Compared with the temporal property, there is a new property violation. From the error trace, we find the problem is that some users want to cancel the video more than half a minute after they watch the video. These behaviours satisfy the temporal property Prop15 but does not satisfy the timing property Prop25.

Table 3 (Prop28 to Prop33) shows the informal descriptions and the corresponding specification patterns of the probabilistic properties of the three case studies. Normally, the SPRT process can correctly accept or reject $H_0$ for these properties. For the probability below 95%, 200 samples are enough to draw the conclusion. When the probability is more than 98%, 200 samples are not enough.

5.2 Performance analysis

In this subsection, we will do a critical performance analysis of the WS-PSC monitor tool. Two key factors that affect the performance of the tool are the AOP-based interceptor and the analyser engine. AOP-based interceptor will affect the execution time of the whole service since it will synchronise the execution of the service. As the analyser runs concurrently, the execution time of the whole service should not be affected. As a platform for our experiments, we used a Windows-based PC equipped with a 1.83 GHz dual-core processor and 2 GB of main memory. The WS-BPEL engine (in the experiment we use Eclipse 3.4 with BPEL Designer plug-in) ran on Tomcat 6.0 and Apache ODE.

Intercepting messages using AOP-based technology does not affect too much the performance of the system. We do some experiments to study the time overhead for using AOP-based interception. We first run the original four composite services with different instances and then run the four composite services with AOP and different instances. The results are shown in Fig. 17. From the figure, we can see that this overhead increases linearly with the number of instances. It takes around 5% (6.0, 5.2, 4.0%) overhead when the instances is 10. It will increase to around 10% (10.2, 10.4, 10.4, 10.6%) when the instances is 50. This is also in line with the results from other researchers; the overload for interceptor is within 5% [30] when the instances is small and it will increase to about 10% when the instances increase to about 50.

In the following, we will focus on investigating the performance of the Analyzers. There are two factors that affect their performance. One is the scale of the generated FA or TA, and the other is the number of service instances running in the engine. For the first factor, the detailed statistics for the monitored properties are illustrated in Fig. 18, where ‘#Part.’ denotes the number of partners involved in the composite services; ‘#Mess.’ is the number of messages exchanged between partners; ‘#Cons.’ is the number of unwanted or wanted constraints in the PSC specifications. ‘#State’ is the number of states in the generated automata and ‘#Tran.’ is the number of transitions in the generated automata. For Prop28 to Prop33, they generate the same statistics as those in the experiment for timing properties, and are not shown in Fig. 18. Note that all of the constructed automata have fewer than 73 transitions. For the second factor, we run the service with different service instances.
For the Analyzers of temporal and timing properties, the composite service generates a large number of messages. However, our monitors receive just those within the PSC specifications. Furthermore, the intercepted events are never stored. Thus, we hope that the monitor does not produce a significant performance overhead. For the Analyzer of probabilistic properties, we have to record the run-time information to support SPRT process. Since there are 33 properties, we choose some properties as examples to analyse the performance, whereas the other properties can be analysed in a similar way and are omitted. In the experiments, we use Jconsole [http://java.sun.com/developer/technicalArticles/J2SE/jconsole.html] as a tool to record the heap memory usage, the number of Java classes, the number of collected threads and the CPU usage for the WS-PSC monitor.

The results are collected in Fig. 19. We do the experiments 10 times and record the average value. For the horizontal axis in each figure, numbers 1 to 7 mean the properties Prop1 to Prop7 whereas 8 and 9 mean the improved implementation of Prop8 and Prop9, called Prop′ and Prop′′. Each figure shows the results for five sets of service instances of (10, 20, 30, 40, 50).

When the number of service instances is 10, the consumed memory is about 10.1 to 18.8 M for Prop1 to Prop5; the collected threads and Java classes are almost the same. The consumed CPU usage is from 0.35 to 1.2%. Consequently, the first factor has minimal effect on the performance since the transitions for Prop1 is 12 and for Prop5 is 73 according to Fig. 18. Furthermore, the additional checking of timing information also has minimal effect on the performance. However, when the number of service instances is increased to 50, the consumed memory is 32.6 M for Prop1 and 87.8 M for Prop5; the collected threads and Java classes are also almost the same; the consumed CPU usage is from 2.34 to 15.86%. From the results we can see that the consumed memory, threads, classes and CPU will increase when the number of service instance increases.

For probabilistic properties, the results are shown under number 6 and 7. We can see that the threads and classes are slightly increased due to the running of SPRT process. We first collect all the generated run-time messages, then we do the SPRT process for every 5000 messages. Consequently, the consumed memory and CPU (55.6 M and 20.4% for Prop′) are dramatically increased due to storing a great number of messages. When the number of service instances is 40, the memory and CPU increase to 240.5 M and 89.8%, respectively, for Prop5 and ran out of CPU for Prop′. When the number of service instances is 50, we cannot monitor Prop5 and Prop′ neither.

To resolve this problem, we just store the TA-based checking results. From the results in number 8 and 9, we can see that the tool can monitor Prop8 and Prop9 since the consumed memory and CPU (93.6 MB and 19% for Prop′′) have been dramatically reduced.

5.3 Discussions

The four case studies clearly demonstrate the intuitiveness and expressiveness of our graphical specification PSC, TPSC and PTPSC (i.e. we can use PSC/TPSC/PTPSC to represent 33 properties). Furthermore, WS-PSC monitor can monitor all 33 properties at run-time with reasonable overhead. For temporal and timing properties, the tool has a reasonable overhead when the number of service instances increases from 10 to 50. For probabilistic properties the tool requires more processing and memories, since it needs to record previous results. With an improved implementation, the tool can still monitor these probabilistic properties when the number of service instances is within 50. However, the proposed tool suffers from the following limitations.

5.3.1 Full automation: Although PSC, TPSC and PTPSC have graphical interfaces, they are still somewhat difficult to write and prone to mistakes when the number of messages grows. Consequently, to reduce this limitation, it is important to develop an approach to help designers automatically derive PSC, TPSC and PTPSC specifications from textual requirements in the future.

5.3.2 Scalability: Although the four case studies are from real industrial requirements, they are devised and invented in academic setting. Furthermore, they are small and not complex. Consequently, the scalability of the approach needs to be tested with larger case studies. As already shown in our experiments, the memory, CPU usage, and the time consumed for the monitoring process will increase with the number of properties and the number of the monitored messages; especially for probabilistic monitors since this kind of monitors need to record some historical information. On the one hand, new and efficient monitoring algorithms can be developed to reduce this limitation. On the other hand, the use of multi-core platforms [31, 32] can also reduce this limitation because it can make the monitoring cost acceptable.

5.3.3 Precision: For probabilistic properties, if the monitored probability is inside the indifference region between $H_0$ and $H_1$, the monitoring result is not accurate. Another specific limitation is the ability to monitor probabilistic properties with extreme probabilities (e.g. when the probability is close to 1 or 0). The number of required monitoring samples runs increase dramatically in order to draw a conclusion. New approaches are needed to solve these problems.

6 Related work

In recent years, a large number of approaches have been proposed for monitoring web services compositions. This section will provide a survey of existing tool support for monitoring of behaviour of composite services against various properties.

Baresi et al. [26] propose a framework called dynamo-AOP for monitoring functional and non-functional properties that a service provider should fulfil. Its architecture is based on the dynamic aspectisation of the BPEL engine executing the monitored service compositions, achieved by using AspectJ as an AOP language. They consider an existing monitoring approach based on ALBERT, which is a temporal logic language suitable for asserting both functional and non-functional properties, and show how to obtain the efficient run-time verification of ALBERT formulae.

Manageable and adaptive service composition (MASC) is a policy-based middleware for monitoring and control of composite web services execution [33]. WS-Policy4MASC language extends WS-Policy by defining new types of monitoring and control policy assertions. It is used to specify monitoring properties related to business exceptions and run-time failures. The approach provides synchronous and asynchronous monitoring at both the SOAP messaging layer and the process orchestration layer, greater diversity of monitoring and control constructs, as well as the

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<table>
<thead>
<tr>
<th>Name</th>
<th>Collaboration type</th>
<th>Property specification</th>
<th>Property type</th>
<th>Timeliness</th>
<th>Validation technique</th>
<th>Tool type</th>
<th>Performance measurement</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dynamo [26]</td>
<td>WS-BPEL</td>
<td>logic-based/WSCoL and Timed WSCoL</td>
<td>pre-, post condition, temporal and timing properties</td>
<td>synchronous and asynchronous</td>
<td>assertion checking and timed automata-based trace analysing</td>
<td>available/DynamoAOP</td>
<td>yes</td>
</tr>
<tr>
<td>Erradi et al. [33]</td>
<td>WS-BPEL</td>
<td>XML-based/WS-Policy4 MASC process-based/event calculus</td>
<td>functional and non-functional properties</td>
<td>synchronous and asynchronous</td>
<td>policy checking</td>
<td>internal/MASC Middleware</td>
<td>yes</td>
</tr>
<tr>
<td>Mahbub and Spanoudakis [34]</td>
<td>BPEL4WS</td>
<td>XML-based/SLang scenario-based/UML 2.0 Sequence Diagrams</td>
<td>QoS properties</td>
<td>synchronous</td>
<td>condition evaluation service</td>
<td>Internal</td>
<td>no</td>
</tr>
<tr>
<td>SLAngMon [35]</td>
<td>interaction between a client and the provider</td>
<td>XML-based/SLang interaction between clients and the provider</td>
<td>non-functional properties</td>
<td>asynchronous</td>
<td>timed automata-based technique</td>
<td>available/SLAngMon</td>
<td>yes</td>
</tr>
<tr>
<td>Simmonds et al. [29, 36]</td>
<td>WS-BPEL</td>
<td>scenario-based/UML 2.0 Sequence Diagrams</td>
<td>safety and bounded liveness properties</td>
<td>synchronous</td>
<td>automata-based trace analysing</td>
<td>IBM WebSphere internal</td>
<td>no</td>
</tr>
<tr>
<td>Wang et al. [30]</td>
<td>interaction between clients and the provider</td>
<td>XML-based/WSCDL interaction between clients and the provider</td>
<td>assertion, functional and non-functional properties</td>
<td>synchronous and asynchronous</td>
<td>finite state automata-based checking</td>
<td>internal</td>
<td>yes</td>
</tr>
<tr>
<td>Cremona [39]</td>
<td>BS-BPEL</td>
<td>XML-based/WSLA</td>
<td>functional and non-functional properties</td>
<td>synchronous</td>
<td>implementation specific techniques</td>
<td>industrial/Cremona</td>
<td>no</td>
</tr>
<tr>
<td>Colombo [40]</td>
<td>BS-BPEL</td>
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<td>non-functional properties</td>
<td>asynchronous</td>
<td>manually</td>
<td>industrial/Colombo</td>
<td>no</td>
</tr>
<tr>
<td>Hallé and Villemaire [37]</td>
<td>interaction between a client and the provider</td>
<td>LTL-FO+ interaction between clients and the provider workflow management systems</td>
<td>temporal properties with data</td>
<td>synchronous</td>
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<td>internal</td>
<td>yes</td>
</tr>
<tr>
<td>Pesic et al. [38]</td>
<td>WS-PSC monitor</td>
<td>DECLARE</td>
<td>temporal properties with parameters</td>
<td>synchronous</td>
<td>process mining</td>
<td>available/WS-PSC monitor</td>
<td>yes</td>
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<tr>
<td>WS-PSC monitor</td>
<td>BS-BPEL</td>
<td>PSC/TPSC/PTPSC</td>
<td>temporal, timing and probabilistic properties</td>
<td>asynchronous</td>
<td>automata and TA-based analysing and hypothesis testing</td>
<td>internal</td>
<td>yes</td>
</tr>
</tbody>
</table>
externalisation of monitoring and adaptation actions from definitions of business processes. A prototype is implemented and evaluated on monitoring and adaptation scenarios from a stock trading case study. The performance studies indicate that MASC’s overhead and scalability are acceptable.

Mahbub and Spanoudakis [34] proposed a framework for the run-time verification of requirements of service-based software systems. System events are collected at run time and stored in an event database. The properties are checked by means of an algorithm based on integrity constraint checking in temporal deductive databases. A prototype tool has been developed to demonstrate and evaluate the framework. The approach has been applied in the car rental system service composition. However, no performance measures have been reported.

Raimondi et al. [35] show how timeliness constraints, such as latency, throughput, availability and reliability, in formal service-level agreements can be translated into TA. They attach time stamps to SOAP messages and consider these messages as timed letters. They are then able to reduce the problem of detecting SLA violations to the acceptance of timed words by the TA that have been derived from the SLAs.

Simmonds et al. [36] proposed to use run-time monitoring of conversations between partners as a means of checking behavioural correctness of web services. They identified a subset of UML 2.0 SD as a property specification language and showed that it is sufficiently expressive for capturing safety and liveness properties. By transforming these diagrams to automata, this approach can check finite execution traces against the specification. They showed how the language can be used to specify the specification property system. They described an implementation of the approach as part of an industrial system. Finally, they discussed the experience of specifying and monitoring a number of properties from three existing applications.

Wang et al. [30] proposed an online monitoring approach for web service requirements. It includes a pattern-based specification of service constraints that correspond to service requirements. The monitoring framework uses different probes and agents to collect events and data that are sensitive to requirements. The framework analyses the collected information against the pre-specified constraints, so as to evaluate the behaviour and use of web services. The prototype implementation and the corresponding experiments with a case study show that the approach is effective and flexible, and the monitoring cost is reasonable.

In [37], the authors present an algorithm for the monitoring of run-time message contracts with data. Their properties are expressed in LTL-FO+, an extension of linear temporal logic that allows first-order quantification over the data inside a trace of XML messages. An implementation of this algorithm can transparently enforce an LTL-FO+ specification using Java applet. Violations of the specification are reported on-the-fly and it can prevent erroneous or out-of-sequence XML messages from being exchanged. Experiments on commercial case study indicate that LTL-FO+ is an appropriate language for expressing their message contracts, and that its processing overhead on sample traces is acceptable both for client-side and server-side enforcement architectures.

Pesic et al. [38] propose DECLARE, a prototype of a workflow management system that uses a constraint-based process modelling language for the development of declarative models describing loosely-structured processes. They show how DECLARE can support loosely-structured processes without sacrificing important WFMSs features like user support, model verification, analysis of past executions using process mining techniques and changing models at run-time.

Cremona [39] is a tool from IBM devised to help clients and providers in the negotiation and life-cycle management of WS-agreements. It provides ‘Status Monitor’ component, which helps in deciding whether a negotiation proposal should be accepted or refused, on the basis of system available resources and the terms of an agreement. Once an agreement has been accepted by the client and the provider, its validity is checked at run-time by a ‘Compliance Monitor’, which can detect violations, predict violations to be occurred and take corrective actions.

Colombo [40] provide a lightweight middleware for service-oriented architectures that support BPEL. It can support declarative service descriptions, such as WS-policy. It can intercept messages before they leave the system or before they are processed, and can use a pipe of dedicated policy-specific verifiers to validate messages with respect to a certain policy.

Comparison of existing run-time monitoring approaches is illustrated in Table 4 according to different parameters. These comparison parameters follow the taxonomy in [3] with some modifications or extensions. ‘Collaboration type’ denotes the composition type of services. Our tool follows most of the common approaches and chooses BPEL-based web service composition. ‘Property specification’ indicates the type of property specification used to specify properties. Our tool uses scenario-based notations to represent desired properties in event-based web service composition and provides users with a completely graphical front-end. ‘Property type’ indicates the kind of properties specified by the language (temporal, timing or probabilistic). Our tool can monitor three kinds of properties. ‘Validation technique’ shows the adopted technique used for validation at run-time. Our tool also follows most common approaches and uses automata and time automata-based validation technique. ‘Timeliness’ indicates how the monitoring activity is performed (post-mortem, synchronous or asynchronous). In order to detect errors as early as possible and reduce the performance overhead, our tool chooses asynchronous style. ‘Tool type’ means the approach is supported by a tool (industrial or internal (available) prototype). ‘Performance measurement’ means whether the performance of the tool is measured or not. To demonstrate the generality of our proposed approach, the performance of our tool has been studied based on four case studies.

7 Conclusions and future work

This paper demonstrates the use of WS-PSC tool chain to monitor temporal, timing and probabilistic properties in BPEL-based composite services using existing graphical specification formalisms. Compared to other approaches, our approach provides a completely graphical front-end for software designers so that they do not have to deal with any particular textual and logical formalisms. Furthermore, the four case studies show that our tool can monitor three kinds of properties with reasonable performance. Finally, the comparative performance of different properties and service instances show some important results: (i) The interceptor does not affect too much the performance of the system; (ii) the scale of the generated monitor of analyser has minimal effect on the performance since the number of transitions
for normal properties is within 100; (iii) the checking of probabilistic properties requires more CPU and memories since it needs to record previous results; (iv) the consumed memory and CPU will increase with the number of service instances.

Several directions for future work are possible. First, to automate the elicitation of properties represented by PSC/TPSC/PTPSC specifications could be an interesting future research task. Initial work has already been done by Autili and Pelliccione [41] to automatically derive PSC specifications from textual requirements. Further work is to derive TPSC and PTPSC specifications from textual requirements using the similar idea. Secondly, our monitoring approach detects errors too late, after the failure arises. In the future we plan to advance monitors with the ability to predict and prevent the potential errors. The proposed new approach can ‘look ahead’ in the near execution future, and predict the potential sources of failures. Thirdly, our statistical approach is not accurate if the monitored probability is inside the indifference region between \( H_0 \) and \( H_1 \). In order to deal with this problem, we plan to use Bayesian sequential hypothesis testing technique [42] since it has already shown faster verification ability than state-of-the-art techniques in statistical model checking.

8 Acknowledgements

This work is supported by the National Natural Science Foundation of China (No. 91118007, No. 61021062, No. 61202097, No. 61202136) and by the National 863 High-Tech Program of China (No. 2011AA010103, No. 2012AA011205), China Postdoctoral Science Foundation (Grant No. 2012T50489 and No. 2011M500897), and Specialized Research Fund for the Doctoral Program of Higher Education (No. 201209412009).

9 References

1 WS-BPEL: ‘Web services business process execution language version 2.0, committee specification’ (OASIS, 2007)


7 ITU: ‘Message Sequence Chart (MSC): International Telecom’ (Union, 1999)

8 OMG: ‘UML 2.1.1 superstructure’ (Object Management Group (OMG), 2006)


9 Appendix: The formal semantics of PSC

9.1 Formal denotational semantics of PSC

Before giving the detailed semantics, we first give some explanation of basic concepts and notations used in the semantics. The denotational semantics of an arrowMsg of PSC with constraint can be defined as: \([[[a, \beta, \lambda, b, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

where \(L = C \cdot a \cdot C\) is the message label. \(\alpha, \beta, \gamma \in (L \setminus l)^{\ast}\) are the unwanted, wanted and strict operator respectively. \(\alpha^\lambda\) means strict operator and future unwanted chain constraint. \(\alpha^\gamma\) means wanted chain constraint. \(\alpha^\beta\) means message set exchanged by different services, and \(\alpha^\gamma\) means means past wanted chain constraint whereas \(\alpha^\beta\) means unbounded trace whereas \(\alpha^\gamma\) means past constraint.

9.1.1 Basic semantics: A.1.1–A.1.9 relate to the denotational semantics of Regular Messages. Note that regular message does not have invalidated trace set since it does not need to happen.

A.1.1 Regular message without constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.2 Regular message with strict operator:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.3 Regular message with past unwanted constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.4 Regular message with future unwanted constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.5 Regular message with strict operator and future unwanted message constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.6 Regular message with past unwanted chain constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.7 Regular message with past wanted chain constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

A.1.8 Regular message with future unwanted chain constraint:

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]

\([[[a, \beta, \lambda, \gamma]]]_{\text{AS}} = \{\beta \cdot \lambda \cdot \gamma \cdot \alpha \in (L \setminus l)^{\ast} \wedge \beta \in (L \setminus b)^{\ast}\}\]
constraint:

\[
\begin{align*}
&[[<c, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} \\
&= \{\alpha \cdot l \cup \{\alpha \cdot l \cdot \beta_1 \cdot m_1\} \cup \{\alpha \cdot m_1 \cdot L_2\} \\
&\cup \cdot \cup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1}\}
\end{align*}
\]

where \( \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \)

\[
[[<c, a, C', \lambda, \neq >, \text{nop}>]]^\text{UDC} = \{\alpha \cdot \{\cup \{\beta_1\} \\
\cup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1}\}
\}
\]

where \( \alpha \in (L \setminus \{l\})^* \land (\alpha \in (L \setminus \{m\})^* \land 1 \leq i \leq n - 1) \)

A.9 Regular message with future wanted constraint:

\[
[[<c, a, C', \lambda, \neq >, \text{nop}>]]^\text{VD} = \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1 Regular message with strict operator:

\[
[[<c, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{\alpha \cdot l | \alpha \in (L \setminus \{l\})^* \}
\]

A.1 Regular message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{\alpha | \alpha \in (L \setminus \{l\})^* \}
\]

A.1 Regular message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{UDC} = \{\alpha | \alpha \in (L \setminus \{l\})^* \}
\]

A.1.2 Required message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{l \cdot l | \alpha \in (L \setminus \{l\})^* \}
\]

A.1.2 Required message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{UDC} = \{l \cdot l | \alpha \in (L \setminus \{l\})^* \}
\]

A.1.2 Required message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{ITV} = \{l \cdot l | \alpha \in (L \setminus \{l\})^* \}
\]

A.1.2 Required message with strict operator:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{ITV} = \{l \cdot l | \alpha \in (L \setminus \{l\})^* \}
\]

A.1.3 Required message with future wanted constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.3 Required message with future wanted constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{UC} = \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.3 Required message with future wanted constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{ITV} = \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.4 Required message with strict operator and future wanted message constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.4 Required message with strict operator and future wanted message constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{UDC} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.4 Required message with strict operator and future wanted message constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{ITV} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.5 Required message with past wanted chain constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{VC} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.5 Required message with past wanted chain constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{UDC} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]

A.1.5 Required message with past wanted chain constraint:

\[
[[<r, a, C', \lambda, \neq >, \text{nop}>]]^\text{ITV} = \{l \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_1 \cdot m_2 \cdot \cdots \cdot \beta_{n-1} \cdot m_{n-1} | \alpha \in (L \setminus \{l\})^* \land (\beta \in (L \setminus \{m\})^* \land 1 \leq i \leq n) \land (\gamma \in L^i)^\text{WD}
\]
constraint:
\[
[[<r, a, C', \neq> g, \lambda>, \text{nop}]]^{VC}
= \{\alpha \cdot l\} \bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1\} \bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2\} \\
\bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \ldots \beta_{n-1} \cdot m_{n-1}\}
\]
where \(\alpha \in (L\setminus\{l\})^* \land (\beta_i \in (L\setminus\{m_i\})^* \land (1 \leq i \leq n))
\]

\[
[[<r, a, C, C', \neq> g, \lambda>, \text{nop}]]^{UDC}
= \{\alpha\} \bigcup \{\alpha \cdot l \cdot \beta_1\} \bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2\} \\
\bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \ldots \beta_{n-1} \cdot m_{n-1}\}
\]
where \(\alpha \in (L\setminus\{l\})^* \land (\beta_i \in (L\setminus\{m_i\})^* \land (1 \leq i \leq n - 1))
\]

\[
[[<r, a, C, C', \lambda, \Rightarrow> g>, \text{nop}]]^{VT}
= \{\alpha\} \bigcup \{\alpha \cdot l \cdot \beta_1\} \bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2\} \\
\bigcup \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \ldots \beta_{n-1} \cdot m_{n-1}\}
\]
where \(\alpha \in (L\setminus\{l\})^* \land (\beta_i \in (L\setminus\{m_i\})^* \land (1 \leq i \leq n - 1))
\]

A.1.18 Required message with future wanted chain constraint:

\[
[[<r, a, C, C', \lambda, \Rightarrow> g>, \text{nop}]]^{VC}
= \{\alpha \cdot l \cdot \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \ldots \beta_n \cdot m_n\} \land (\beta_i \in (L\setminus\{m_i\})^* \land (1 \leq i \leq n - 1))
\]

A.1.19 Fail message without constraint:

\[
[[<f; a, C, C', \lambda, \lambda>, \text{nop}]]^{VC}
= \{\alpha \cdot l \in (L\setminus\{l\})^*\}
\]

A.1.20 Fail message with strict operator:

\[
[[<f; a, C, C', \lambda, \lambda>, \text{strict}]]^{VC}
= \{\alpha \cdot l \in (L\setminus\{l\})^*\}
\]

A.1.21 Fail message with past unwanted constraint:

\[
[[<f; a, C, C', \lambda, \lambda>, \text{nop}]]^{UDC}
= \{\beta \in \{\alpha \cdot \gamma\}, \text{ where } \beta \in b^\omega \land \alpha \in (L\setminus\{b\})^* \land \gamma \in (L\setminus\{l\})^\omega\}
\]

A.1.22 Fail message with past unwanted chain constraint:

\[
[[<f; a, C, C', \lambda, \lambda>, \text{nop}]]^{VT}
= \{\alpha \cdot l \in (L\setminus\{l\})^*\}
\]

A.1.19–A.1.23 relate to the denotational semantics of Fail messages.

A.1.22 Fail message with past unwanted chain constraint:

(see equation at the bottom of the page)
Table 5 Correspondence between denotational semantics and operational semantics of PSC

<table>
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<tr>
<th>denotational semantics</th>
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<tbody>
<tr>
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<td>denotational semantics</td>
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<tr>
<td>operational semantics</td>
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<td>RR2</td>
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<td>RR4</td>
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<tr>
<td>denotational semantics</td>
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<tr>
<td>operational semantics</td>
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<td>FR5</td>
<td>Def 4</td>
<td>Def 5</td>
<td>Def 6</td>
<td>Def 7</td>
</tr>
</tbody>
</table>

A.1.23 *Fail* message with past wanted chain constraint:
\[
[(\langle f', a, C, C', \not\to g, \lambda >, \text{nop} \rangle)]^{\text{VC}}
\]
\[
= \{ \beta_1 \} \cup \{ \beta_1 \cdot m_1 \cdot \beta_1 \} \cup \ldots
\]
\[
\cup \{ \beta_1 \cdot m_1 \cdot \beta_2 \cdot \ldots \cdot \beta_{n-1} \cdot m_{n-1} \cdot \gamma \}
\]
where \( \beta_1 \in (L \setminus \{m_i\})^* \land (1 \leq i \leq n) \land
\]
\( (\gamma \in (L \setminus \{l\})^*) \)

9.1.2 *Compositional semantics:* A.2.1 'Merge' operator is used to compose the denotational semantics of two continuous messages. The semantics can be defined as follows: (see equation at the bottom of the page)
\[
[[msg_1 \cdot msg_2]]^{\text{UDC}} = [[msg_1]]^{\text{UDC}} \cup [[msg_2]]^{\text{UDC}}
\]
\[
[[msg_1 \cdot msg_2]]^{\text{IVT}} = [[msg_1]]^{\text{IVT}} \cup [[msg_2]]^{\text{IVT}}
\]

A.2.2 'Alternative' operator is used to compose the denotational semantics of two alternative messages. The semantics can be defined as follows:
\[
[[msg_1 \otimes msg_2]]^{\text{VC}} = [[msg_1]]^{\text{VC}} \cup [[msg_2]]^{\text{VC}}
\]
\[
[[msg_1 \otimes msg_2]]^{\text{UDC}} = [[msg_1]]^{\text{UDC}} \cup [[msg_2]]^{\text{UDC}}
\]
\[
[[msg_1 \otimes msg_2]]^{\text{IVT}} = [[msg_1]]^{\text{IVT}} \cup [[msg_2]]^{\text{IVT}}
\]

Based on Merge and Alternative, the operators Alt, Par and Loop can be defined as follows:
\[
[[\langle f', a, C, C', \not\to g, \lambda >, \text{nop} \rangle]]^{\text{IVT}}
\]
\[
= \{ \beta_1 \cdot l \} \cup \{ \beta_1 \cdot m_1 \cdot \beta_2 \cdot l \} \cup \ldots
\]
\[
\cup \{ \beta_1 \cdot m_1 \cdot \beta_2 \cdot m_2 \cdot \ldots \cdot \beta_{n-1} \cdot m_{n-1} \cdot \gamma \cdot l \}
\]
where \( \beta_1 \in (L \setminus \{m_i\})^* \land (1 \leq i \leq n) \land
\]
\( (\gamma \in (L \setminus \{l\})^*) \)
\[
[[msg_1 \cdot msg_2]]^{\text{VC}} = \{ \alpha_1 \cdot \alpha_2 \mid \alpha_1 \in [[msg_1]]^{\text{VC}} \land \alpha_2 \in [[msg_2]]^{\text{VC}} \}
\]

A.2.3 *Alt* operator: \( \text{Alt}(\omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r}, r) \), where \( \omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r} \) means \( r \) part alternatives. The definition of Alt operator can be composed by Merge and Alternative composition.
\[
[[\text{Alt}(\omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r}, r)]]^{\text{trace}} = [[[\omega_1^{l_1}]]^{\text{trace}} \otimes \ldots \otimes [[[\omega_r^{l_r}]]^{\text{trace}}]^{\text{trace}}]
\]
where
\[
[[\omega_i^{l_i}]]^{\text{trace}} = [[[msg_{i_1}]]^{\text{trace}} \otimes [[[msg_{i_2}]]^{\text{trace}}]^{\text{trace}} \otimes \ldots
\]
\[
\otimes [[[msg_{i_{r-1}}]]^{\text{trace}}]^{\text{trace}} \otimes [[[msg_{i_r}]]^{\text{trace}}]^{\text{trace}}]
\]
\( 1 \leq k \leq r \)

A.2.4 *Par* operator: \( \text{Par}(\omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r}, r) \), where \( \omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r} \) means \( r \) parts to parallel. The definition of Par operator can also be composed by Merge and Alternative composition.
\[
[[\text{Par}(\omega_1^{l_1}, \omega_2^{l_2}, \ldots, \omega_r^{l_r}, r)]]^{\text{trace}} = [[[\omega_1^{l_1}]]^{\text{trace}} \otimes \ldots \otimes [[[\omega_r^{l_r}]]^{\text{trace}}]^{\text{trace}}]
\]
where
\[
[[\omega_i^{l_i}]]^{\text{trace}} = [[[msg_{i_1}]]^{\text{trace}} \otimes [[[msg_{i_2}]]^{\text{trace}}]^{\text{trace}} \otimes \ldots
\]
\[
\otimes [[[msg_{i_{r-1}}]]^{\text{trace}}]^{\text{trace}} \otimes [[[msg_{i_r}]]^{\text{trace}}]^{\text{trace}}]
\]
\( 1 \leq k \leq \text{num(Par)} \)

A.2.5 *Loop* operator: \( \text{Loop}(a, m, n) \), \( a \) is the loop body and \( m \) and \( n \) are the lower and upper bound of loop, respectively. The definition of Loop operator can also be composed by Merge and Alternative composition.
\[
[[\text{Loop}(a^{l_0}, m, n)]]^{\text{trace}} = [[[\omega']]^{\text{trace}} \otimes 2[[\omega']]^{\text{trace}} \otimes \ldots \otimes (m - \omega')[[\omega']]^{\text{trace}}]
\]
where
\[
[[\omega']]^{\text{trace}} = [[[msg_{i_1}]]^{\text{trace}} \otimes [[[msg_{i_2}]]^{\text{trace}}]^{\text{trace}} \otimes \ldots
\]
\[
\otimes [[[msg_{i_{r-1}}]]^{\text{trace}}]^{\text{trace}} \otimes [[[msg_{i_r}]]^{\text{trace}}]^{\text{trace}}]
\]
\( k \) times

\[\text{k}[[\omega']]^{\text{trace}} = \frac{[[\omega']]^{\text{trace}} \otimes [[\omega']]^{\text{trace}} \otimes \ldots \otimes [[\omega']]^{\text{trace}}}{k \text{ times}}\]
9.2 Consistency between denotational and operational semantics of PSC

Theorem 1: Given a PSC composed by basic rules and operational rules, the denotational semantics and operational semantics are consistent. In detail, it means that:

- The traces accepted by the glue states of FAs of operational semantics equal the validated continuations defined by denotational semantics.
- The traces accepted by the intermediate states of FAs of operational semantics equal the undecided continuations defined by denotational semantics.
- The traces accepted by the accepting states of FAs of operational semantics equal the invalidated traces defined by denotational semantics.

Proof: The proof is composed of the following three steps:

- In the first step, we prove the consistency between operational semantics and denotational semantics of basic arrowMsg (with strict operators or other constraints). This can be proved by the corresponding basic operational semantics to basic denotational semantics. Table 5 shows the corresponding relations between operational semantics and denotational semantics of basic messages. We use A.1.1 and ER1 as an example.

  A.1.1 \(\Leftrightarrow\) ER1

According to the FA generated by ER1, the finite traces accepted by the glue state can be described by the following regular expression: \(\langle l \rangle^\infty \cdot l = (L/\{l\})^\infty \cdot l\), which equals \([\{l.a\}]_{\text{EC}} = \{a \mid a \in (L/\{l\})^\infty\}\). That is to say, the traces accepted by the glue states of FAs of operational semantics equal the validated continuations defined by denotational semantics.

According to the FA generated by ER1, the finite traces accepted by the intermediate state can be described by the following regular expression: \(\langle l \rangle^m = (L/\{l\})^m\), which equals \([\{l.a\}]_{\text{IDF}} = \{a \mid a \in (L/\{l\})^m\}\). That is to say, the traces accepted by the intermediate states of FAs of operational semantics equal the undecided continuations defined by denotational semantics.

- The second step is to prove the consistency of Merge and Alternative composition. According to the definition of operational semantics, Merge composition keeps the accepting traces of other states and deletes the glue state of the previous state. Consequently, the accepting traces of the glue states of the newly generated FA is the connection of those in the two FAs. The accepting traces of other states of the new generated FA are the union of those in the two FAs. This is the same as the definition of denotational semantics of Merge. According to the definition of operational semantics, Alternative composition alternatively selects the accepting traces of those in the two states. Consequently, the accepting traces of the states of the new generated FA is the alternative selection of those in the two FAs. This is also the same as the definition of denotational semantics of Alternative.
- The third step is to prove the consistency of operators Alt, Par and Loop. According to the operational semantics, Alt operator first uses Merge composition to generate \(k\) alternatives, then repeatedly uses Alternative composition to generate the final FA. Consequently, the validated traces, undecided traces and invalidated traces are alternative selection of these \(r\) parts, which is the same definition of denotational semantics. According to the operational semantics, Par operator first uses Merge composition to generate \(\text{num(Par)}\) alternatives, then repeatedly uses Alternative composition to generate the final FA. Consequently, the validated traces, undecided trace and invalidated trace are alternative selection of these \(\text{num(Par)}\) parts, which is the same definition of denotational semantics. After these three steps, we can prove that the PSC specifications composed by basic rules and operational rules have consistent operational and denotational semantics. Consequently, Theorem 1 is proved.